SLFS001A - JUNE 1985 - REVISED APRIL 1988

- Adjustable Gain to 400 Typ
- No Frequency Compensation Required
- Low Noise ... 3 μV Typ V_n

description

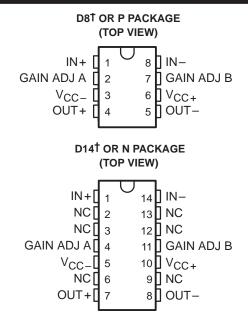
This device is a monolithic two-stage video amplifier with differential inputs and differential outputs. It features internal series-shunt feedback that provides wide bandwidth, low phase distortion, and excellent gain stability. Emitterfollower outputs enable the device to drive capacitive loads. All stages are current-source biased to obtain high common-mode and supplyvoltage rejection ratios.

The differential gain is typically 400 when the gain adjust pins are connected together, or amplification may be adjusted for near 0 to 400 by the use of a single external resistor connected between the gain adjustment pins A and B. No external frequency-compensating components are required for any gain option.

The device is particularly useful in magnetic-tape or disk-file systems using phase or NRZ encoding and in high-speed thin-film or plated-wire memories. Other applications include generalpurpose video and pulse amplifiers.

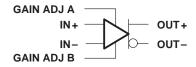
The device achieves low equivalent noise voltage through special processing and a new circuit layout incorporating input transistors with low base resistance.

The TL592B is characterized for operation from 0°C to 70°C.



[†] D8 and D14 are the codes to differentiate the 8-pin and 14-pin versions, respectively.

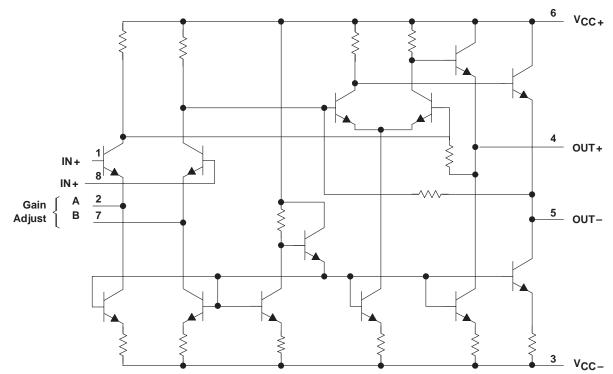
symbol





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schematic



Pin numbers are for D8 and P packages.

absolute maximum ratings over operating free-air temperature (unless otherwise noted)

Supply voltage, V _{CC+} (see Note 1)	
Supply voltage, V _{CC}	8 V
Differential input voltage	±5 V
Voltage range, any input	\dots V _{CC+} to V _{CC-}
Output current	10 mA
Continuous total power dissipation	See Dissipation Rating Table
Operating free-air temperature range	0°C to 70°C
Storage temperature range	–65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

NOTES: 1. All voltage values except differential input voltages are with respect to the midpoint between V_{CC+} and V_{CC-} .

DISSIPATION RATING TABLE							
PACKAGE	T _A ≤ 25°C POWER RATING	DERATING FACTOR	DERATE ABOVE T _A	T _A = 70°C POWER RATING			
D8	530 mW	5.8 mW/°C	59°C	464 mW			
D14	530 mW	N/A	N/A	530 mW			
N	530 mW	N/A	N/A	530 mW			
Р	530 mW	N/A	N/A	530 mW			



recommended operating conditions

	MIN	NOM	MAX	UNIT
Supply voltage, V _{CC+}	3	6	8	V
Supply voltage, V _{CC} _	-3	-6	-8	V
Operating free-air temperature, T _A	0		70	°C

electrical characteristics at specified free-air temperature, V_{CC\,\pm} = ± 6 V, R_L = 2 k Ω (unless otherwise noted)

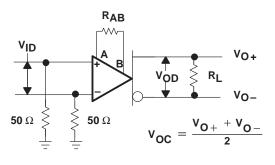
PARAMETER		TEST FIGURE	TEST CONDI	TIONST	TA	MIN	ТҮР	МАХ	UNIT	
A	Large-signal differential		V _{OPP} = 3 V,	$R_L = 2 k\Omega$,	25°C	300	400	500		
AVD	voltage amplification				0°C to 70°C	250		600	V/V	
A _{VD2}	Large-signal differential voltage amplification	1	V _{OPP} = 3 V, R _{AB} = 1 kΩ	R _L = 2 kΩ,	25°C		13		V/V	
BW	Bandwidth (-3 dB)	2	V _{OPP} = 1 V,	$R_{AB} = 0$	25°C		50		MHz	
	Input offerst surrent				25°C		0.4	5		
10	Input offset current				0°C to 70°C			6	μA	
	lanut hing gumant				25°C		9	30	A	
IВ	Input bias current				0°C to 70°C			40	μA	
\/	Common-mode input	2			25°C	±1			v	
VICR	voltage range	3			0°C to 70°C	±1				
Voc	Common-mode output voltage	1	RL = ∞		25°C	2.4	2.9	3.4	V	
. ,	0		V _{ID} = 0,	R _{AB} = ∞,	25°C		0.35	0.75		
V00	Output offset voltage	1	$R_L = \infty$	AD .	0°C to 70°C			1.5	V	
	Peak-to-peak output	25°C	25°C	3	4					
VOPP	voltage swing	1	$R_L = 2 k\Omega$,	$R_{AB} = 0$	0°C to 70°C	2.8			V	
					25°C		4		kΩ	
ri	Input resistance		V _{OD} = 1 V,	$R_{AB} = 0$	0°C to 70°C		3.6			
r _o	Output resistance				0°C to 70°C			30	Ω	
C _i	Input capacitance				25°C		5		pF	
				f = 100 kHz	0500	60	86			
	Common-mode rejection		$V_{IC} = \pm 1 V$,	f = 5 MHz	25°C		60			
CMRR	ratio	3	$R_{AB} = 0$	f = 100 kHz	000 / 7000	50			dB	
				f = 5 MHz	0°C to 70°C		60			
	Supply voltage rejection		ΔV_{CC} + = ± 0.5 V,	$CC + = \pm 0.5 V, R_{AB} = 0$	25°C	50	70		dB	
^k SVR	ratio ($\Delta V_{CC} / \Delta V_{IO}$)	4	$\Delta V_{CC} - = \pm 0.5 \text{ V},$		0°C to 70°C	50				
V _n	Broadband equivalent input noise voltage	4	BW = 1 kHz to 10 MHz		25°C		3		μV	
^t pd	Propagation delay time	2	$\Delta V_{O} = 1 V$		25°C		7.5		ns	
t _r	Rise time	2	$\Delta V_{O} = 1 V$		25°C		10.5		ns	
lsink(max)	Maximum output sink current		V _{ID} = 1 V,	V _O = 3 V		3	4		mA	
ICC	Current aurorat	+		No signal	25°C		18	24		
	Supply current		No load,		0°C to 70°C			27	mA	

[†] R_{AB} is the gain-adjustment resistor connected between gain-adjust pins A and B. If not specified for a particular parameter, its value is irrelevant to that parameter.

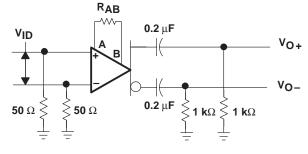


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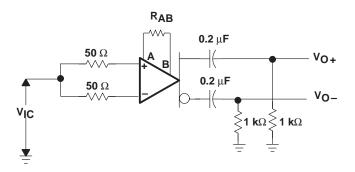
PARAMETER MEASUREMENT INFORMATION











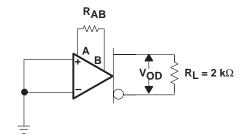


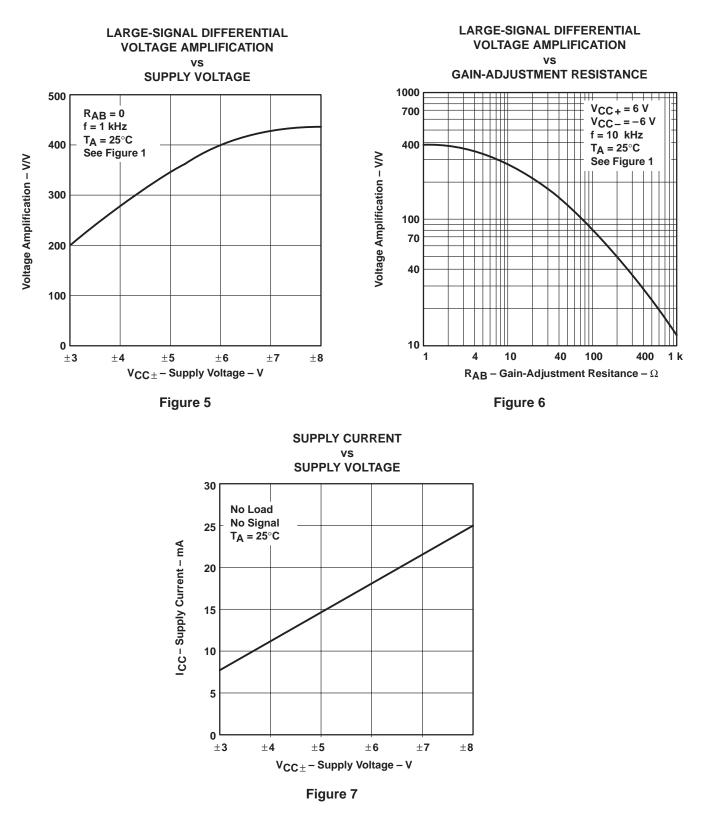
Figure 3

Figure 4



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TYPICAL CHARACTERISTICS





PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
TL592B-8D	ACTIVE	SOIC	D	8	75	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
TL592B-8DR	ACTIVE	SOIC	D	8	2500	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
TL592BI-8D	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL592BN	OBSOLETE	PDIP	Ν	14		None	Call TI	Call TI
TL592BP	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL592BPSR	ACTIVE	SO	PS	8	2000	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - May not be currently available - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

None: Not yet available Lead (Pb-Free).

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean "Pb-Free" and in addition, uses package materials that do not contain halogens, including bromine (Br) or antimony (Sb) above 0.1% of total product weight.

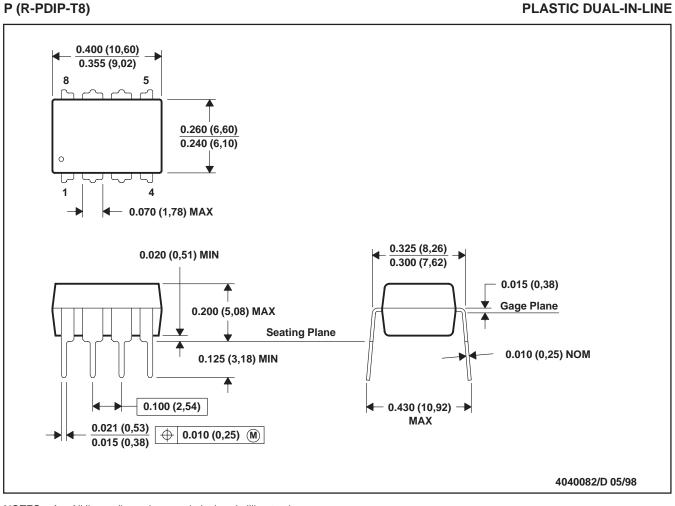
⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDECindustry standard classifications, and peak solder temperature.

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MECHANICAL DATA

MPDI001A - JANUARY 1995 - REVISED JUNE 1999



- NOTES: A. All linear dimensions are in inches (millimeters).
 - B. This drawing is subject to change without notice.
 - C. Falls within JEDEC MS-001

For the latest package information, go to http://www.ti.com/sc/docs/package/pkg_info.htm



N (R-PDIP-T**)

PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



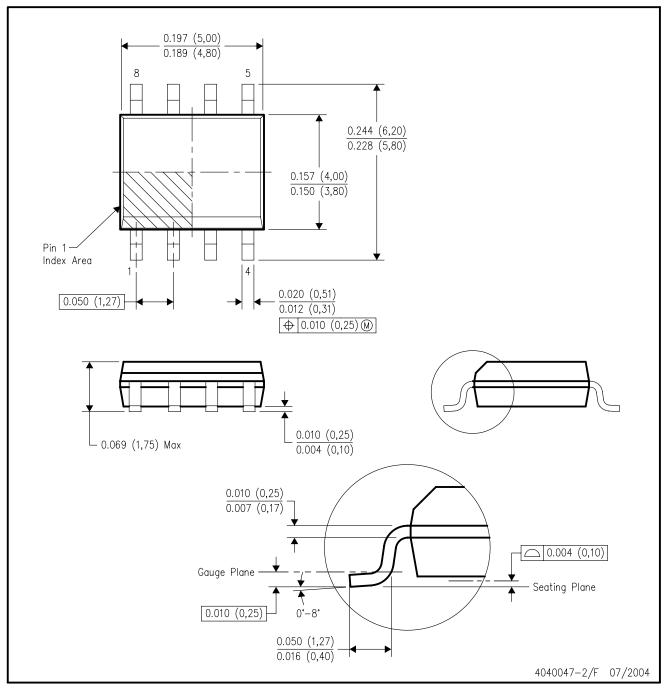
NOTES:

- A. All linear dimensions are in inches (millimeters).B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- \triangle The 20 pin end lead shoulder width is a vendor option, either half or full width.



D (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).

D. Falls within JEDEC MS-012 variation AA.



MECHANICAL DATA

PS (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



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